



#8114/Electronics
6-501
R. Grader

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Mark W. Miles

Art Unit : 2873

Serial No. : 09/413,222

Examiner : H. Dang

Filed : October 5, 1999

Title : PHOTONIC MEMS AND STRUCTURES

Commissioner for Patents
Washington, D.C. 20231

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RESPONSE TO ACTION DATED FEBRUARY 27, 2001

Please amend claim 5 as follows:

1. β . (amended) A line-at-a-time electronic driving method comprising
applying a bias voltage to rows ^{or columns} of a device,
applying data voltages to the columns ^{or rows} alternately about a value of the bias
voltage,
actuation of the device occurring when a difference between the values of the data
voltage and a select voltage is above a first predetermined level,
release of the device occurring when the difference between the values of the data voltage
and the select voltage is below a second predetermined level lowest, and
the device maintaining its state when the select voltage is at the bias level.

Add claims 52 through ⁶⁰ 57:

3-⁵² 52. The method of claim ⁵¹ β in which the bias voltage is within a hysteresis window of
the device.

4-⁵³ 53. The method of claim ⁵² β in which the bias voltage is in the middle of the
hysteresis window.

54. The method of claim 51 in which the first predetermined threshold is about 9 volts
and the second predetermined threshold is about 0 volts.

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06/01/2001 RRRADEN 00000003 061050 09413222

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